

Technocrats Institute of Technology, Bhopal

## **Minutes of 1<sup>st</sup> Meeting of Board of Studies (BoS) in Electronics & Communication Engineering**

**Date:30.08.2025**

### **(After the grant of Autonomy by UGC)**

The 1<sup>st</sup> meeting of Board of Studies (BoS) in Electronics & Communication Engineering of Technocrats Institute of Technology, Bhopal was held on 30<sup>th</sup> August 2025 at 2.30 PM in hybrid mode at Senate Hall, Central Block of the Technocrats Institute of Technology. The meeting was chaired by Dr. Girraj P Rathor (Chairman, BoS (ECE)). The following members were present in the meeting:

S. No.	Name of the member	Category/Position in the BoS	Mode of Attendance
1	Dr. Girraj P. Rathor	Chairman	Off line
2	Dr. Madhu Shandilya (Professor (ECE) MANIT Bhopal)	Member (subject expert from outside the parent university)	On line
3	Dr. Vijayshri Chaurasia (Associate Professor (ECE) MANIT Bhopal)	Member (subject expert from outside the parent university)	On line
4	Dr. Prabhat Patel (Professor (ECE) RGPV Bhopal)	Member (subject expert nominated by the Vice-Chancellor of the affiliating university)	Off line
5	Er. Abhigyanam Giri (Industry Representative)	Member	On line
6	Ms Megha Chaturvedi (College Alumni)	Member	Off line
7	Prof. Sandip Nemade	Member Secretary	Off line

## Minutes of Meetings

- Meeting of the Board of Studies of B.Tech. (ECE), M.Tech. (DC) and M.Tech. (VLSI) of Technocrats Institute of Technology, Bhopal The meeting commenced with a welcome note by the Chairman, Dr. Girraj P Rathor, who greeted all members and highlighted the objectives of the 1st BoS meeting.
- The Chairman presented the proposed scheme and syllabi for the undergraduate (B.Tech. – Electronics & Communication Engineering) and postgraduate (M.Tech. – Digital

Communication & M.Tech. – Microelectronics & VLSI Design) programs.

प्रायोगिक  
इलेक्ट्रॉनिक्स एवं प्रॉग्रामिंग  
Department of Electronics & Computer Engineering  
एम.पी.आई.टी., भोपाल-462006 (M. P.)  
M.A.N.I.T., Bhopal-462006 (M. P.)

<b>Discussion</b>	<b>Resolution</b>
<ol style="list-style-type: none"> <li>1. The proposed scheme and syllabi under UGC autonomy for the program B.Tech. (Electronics &amp; Communication Engineering) M. Tech. (VLSI) and M. Tech. (DC) are presented.</li> <li>2. Alignment of scheme and syllabi with the National Education Policy (NEP 2020).</li> <li>3. A balanced mix of theory and practical subjects as per the AICTE Model curricula.</li> <li>4. Inclusion of subjects from emerging areas to keep pace with technological advancements.</li> <li>5. Removal of outdated subjects and topics to make the curriculum more relevant.</li> <li>6. Integration of interdisciplinary subjects to enhance knowledge cross functional domains.</li> </ol>	Various suggestion and correction for the improvement in the proposed scheme and syllabi given by the members of the BoS have been duly incorporated.

#### **Key Suggestions:**

1. Engineering Mathematics with the specific topics pertaining to the branch should be included till IV Semester.
2. Critical reasoning and cognitive ability level-1, 2, 3, 4, 5, 6 may be include till VIth Semester.
3. A Course in Electromagnetic theory may be include in IVth or Vth Semester.
4. Signal & System and Digital Electronics and logic design may be swapped.
5. Probability and stochastic process may be included in Vth Semester Elective and Electronic Instrumentation & Measurement be included in VIIth Sem Elective.
6. In all the practical's, paper publication must be replaced by "current research presentation/ paper publication." *habhab*
7. Digital Image processing should be added as Elective in higher semester. *Verdict by*

*Shashi Hemadev* *Minal* *final* *Dr. G.P. RATHOR*  
*Sandeep Hemadev* *Minal* *final* *Dr. G.P. RATHOR*

*Shashi* *(Dr. V. Chaurasia)* *Dr. Mahu Shandilya*  
*Dr. Mahu Shandilya* *Professor*  
*इलेक्ट्रॉनिक्स एवं कम्प्यूटर इंजीनियरिंग विभाग*  
*Department of Electronics & Communication Engineering*  
*महाराष्ट्र विश्वविद्यालय, भोपाल-462003 (म.प.)*  
*M.I.T., Bhopal-462003 (M. P.)*

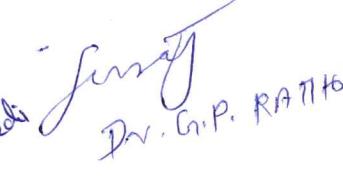
### Action Taken:

All the above suggestions were unanimously agreed upon and have been taken into consideration for incorporation in the revised curriculum.

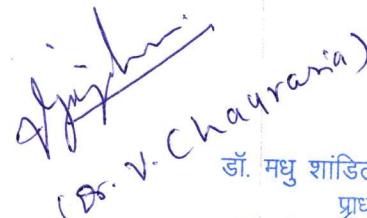
All the faculty members of the department were also present in the meeting. The meeting ended with a **vote of thanks to the Chair**, expressing gratitude for the valuable contributions of all members.

  
Sandip  
Hemade

  
Megha  
Chatterjee

  
Dr. G.P. Rautela

  
Arshad

  
(Dr. V. Chaurasia)

डॉ. मधु शांडिल्य / Dr. Madhu Shandilya  
प्राध्यापक / Professor  
इलेक्ट्रॉनिक्स (एवं कम्प्युनेक्शन इंजीनियरिंग विभाग  
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